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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of

SHIMAMOTO *et al.*

Application Number: 10/020,314

Filed: December 18, 2001

For: FABRICATING METHOD OF
SEMICONDUCTOR INTEGRATED CIRCUITS

Attorney Docket No. HITA.0143

Commissioner of Patents

P.O. Box 1450

Alexandria, VA 22313-1450

DECLARATION OF NANCY DESANTI

Sir:

I, Nancy DeSanti, hereby declare the following:

(1) I have been employed by Reed Smith LLP at the Falls Church Office located at 3110 Fairview Park Dr., Falls Church, Virginia 22043 since January 1997, and during that time I have been a Legal Secretary and an IP Secretary, and I have worked in the IP Division from September 1997 to the present.

(2) Among my responsibilities, I am in charge of opening all incoming mail, including all Office Actions, Notices of Allowance and other communications issued by the U.S. Patent and Trademark Office (USPTO) in connection with all patents and patent applications being prosecuted, monitored or otherwise handled by our Falls Church IP Division office. I am also in charge of docketing all due dates associated with any such Office Actions, Notices of Allowances and other communications received from the USPTO.

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(3) Prior to the telephone call from the Examiner on September 20, 2005, in connection with the above-referenced application, our office had not received any communication from the USPTO after the Office Action mailed October 14, 2004 (to which a Response was filed January 6, 2005). Specifically, no Final Rejection was received. A copy of the docketing sheet is submitted herewith showing that no Office Action with a mailing date of March 23, 2005, was ever received and entered by our office. The only entry after the response of January 6, 2005, is for the receipt of the facsimile copy of the Office Action sent by Examiner Nguyen on November 4, 2005.

(4) I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine, or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the above-captioned application or any registration resulting therefrom.

Date: 2/2/06


Nancy DeSanti

REED SMITH LLP
3110 Fairview Park Drive, Suite 1400
Falls Church, Virginia 22042
(703) 641-4200

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Application Number:

10/020,314 Fabricating method of semiconductor integrat

Transaction History

Date	Content
03-23-2005	Mail Final Rejection (PTOL - 32)
01-27-2005	Date Forwarded to Examiner
01-08-2005	Workflow Incoming amendmen
10-14-2004	Non-Final Rejection
08-13-2004	IFW TSS Processing by Tech C
08-03-2004	Request for Continued Examin
04-20-2004	Correspondence Address Cha
01-08-2004	Mail Advisory Action (PTOL - 3
12-23-2003	Date Forwarded to Examiner
10-21-2003	Mail Final Rejection (PTOL - 32
08-28-2003	Date Forwarded to Examiner
05-06-2003	Mail Non-Final Rejection

HT#	HITA-0143USA		TITLE		FABRICATING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUITS		PRIORITY		01/31/2001		12/18/2001		12/18/2001	
COUNTRY	US	UNITED STATES	SERIAL#		10/020,314		MAIL		12/18/2001		12/18/2001		12/18/2001	
NEWCOM	NEW		PATENT#				FILE		12/18/2001		12/18/2001		12/18/2001	
RELATED			STATUS		PENDING		PUBL							
TYPE	UTL		HITACHI				ISSUE							
CLIENT	HITA		CREF		310002329US1		1ST							
AGENT			AREF				EXP							

ID	O	ACTION	BASE	DUE IN	DUE	EXTRA	FINAL	EXT	RESPONSE	CALL UP	1	2	P
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G2	N	MISSING PARTS (2 MO)	01/16/2002	2/M	03/16/2002	4/1	07/16/2002	0	01/28/2002		1/M	Y	Y
DS	N	INF DISCLOSURE STMT	12/18/2001	3/M	03/18/2002		03/18/2002	0			1/M	Y	Y
FS	N	FILE RCPT STATUS CHK	12/18/2001	3/M	03/18/2002		03/18/2002	0	01/16/2002		0/M	Y	Y
Updated filing receipt mailed 2/8/02; corrected filing receipt mailed 10/7/02													
PU	N	PUBLICATION	01/31/2001	18/M	07/31/2002		07/31/2002	0	08/01/2002		1/M	Y	Y
am	N	AMEND PUBLICATION	09/01/2002	2/M	10/01/2002		10/01/2002	0			1/M	Y	Y
RT	N	RESPONSE TO OA (IMO)	02/28/2003	1/M	03/28/2003	5/1	08/28/2003	1	03/28/2003		0/M	Y	Y
ap	N	AMEND PREPUBLICATION	12/18/2001	17/M	05/18/2003		08/18/2003	0	08/01/2002		1/M	Y	Y
SC	N	STATUS CHECK	12/18/2001	18/M	08/18/2003		08/18/2003	0			1/M	Y	Y
RE	N	RESPONSE TO OA	05/08/2003	3/M	08/08/2003	3/1	11/08/2003	1	08/08/2003		1/M	Y	Y
NA	N	NOTICE OF APPEAL	10/21/2003	3/M	01/21/2004	3/1	04/21/2004	0	01/18/2004		1/M	Y	Y
Advisory action mailed 1/6/04; ROE filed 1/18/04													
FR	N	FINAL REJECTION	10/21/2003	3/M	01/21/2004	3/1	04/21/2004	0	12/18/2003		1/M	Y	Y
RE	N	RESPONSE TO OA	10/14/2004	3/M	01/14/2005	3/1	04/14/2005	0	01/08/2005		1/M	Y	Y
NA	N	NOTICE OF APPEAL	11/04/2005	3/M	02/04/2006	3/1	05/04/2006	0			1/M	Y	Y
FR	N	FINAL REJECTION	11/04/2005	3/M	02/04/2006	3/1	05/04/2006	0			1/M	Y	Y
(Waiting for official re-entrant date (copy of OA faxed in advance))													

ASSIGNEES
Renesas Technology Corporation

INVENTORS
SHIMAMOTO, Yasuhiro
HIRATANI, Masahiko
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YAMAMOTO, Satoshi
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ANDO, Toshio
SAKUMA, Hiroshi
IJIMA, Shinsai

USER-DEFINABLE FIELDS			
PUB#	SMALL ENTITY	ART UNIT	
TXTZ	CLAIMS	EXAMINER	
CASE	PUBLICATION#	CONFIRM#	
P01452US00		ENTERED 12/13/2001 MODIFIED 01/31/2006 BY NDS	
		ATTORNEYS	
		SPF	
		JCM	
		SPF	

NOTES
Jennifer Teng work stay case; Reassignment to Reneess Technology Corporation filed October 1, 2003; advisory action mailed 1/6/04; RCE filed 1/16/04; waiting for examiner to restart date (office action dated 3/29/05 never received)

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10/460,155 MANUFACTURING METHOD FOR SEMICONDUCTOR

Transaction History

Date	Content
12-20-2005	Recordation of Patent Grant M
12-20-2005	Patent Issue Date Used in PTA
11-10-2005	Dispatch to FDC
11-08-2005	Application is Considered Rea
11-07-2005	Receipt into Pub
10-03-2005	Record Patent Decision of Gr of Issue Fee
09-14-2005	Petition Entered
08-26-2005	Abandonment for Failure to Pa
04-25-2005	Workflow - File Sent to Contra
03-21-2005	Notice of Allowance Data Verif
11-18-2004	Date Forwarded to Examiner
11-05-2004	Request for Extension of Time

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HT#	HITA-0401 USA		UNITED STATES		TITLE		MANUFACTURING METHOD FOR SEMICONDUCTOR DEVICES		PRIOR	07/25/2002
COUNTRY	US	NEW	SERIAL#	10480,155	MAIL	08/13/2003	FILE	08/13/2003	FILE	08/13/2003
RELATED			PATENT#	6,977,229					PUBL	12/20/2005
TYPE	UTL		STATUS	ISSUED					ISSUE	08/13/2003
CLIENT	HITA	HITACHI			1	CHIEF	310200877US1		1ST	08/13/2003
AGENT						AREF			EXP	08/13/2023

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FS	N	FILG RCPT STATUS CHK		08/13/2003	3M	08/13/2003		0	08/13/2003		0M	Y	Y
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SC	N	STATUS CHECK		08/01/2005	21d	09/22/2005		0	09/05/2005		1M	Y	Y
Check whether client has rec'd for reviving abandoned app (NOA never rec'd); client must to revive rec'd 8/6/05													
PN	N	PUBLICATION FEE		10/03/2005	1M	11/03/2005		0	10/31/2005		1M	Y	Y
Due date of 11/03/05 provided in decision on petition to revive													
IF	N	ISSUE FEE		10/03/2005	1M	11/03/2005		0	10/31/2005		1M	Y	Y
Due date of 11/03/05 provided in decision on petition to revive													
AJ	N	REQ PATENT TERM ADJ		12/20/2005	0M	12/20/2005		0	12/20/2005		1M	Y	Y
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M3	N	3RD MAINT FEE DUE		12/20/2005	138M	08/20/2017	16	0	12/20/2017		3M	Y	Y

INVENTORS		ASSIGNEES	
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MOMONOI, Yoshinori			
IZAWA, Naotaru			

USER-DEFINABLE FIELDS		PATENT FIELDS	
PUB#		SMALL ENTITY	No
TXT2		CLAIMS	
CASE		PUBLICATION#	
ENTERED 06/10/2003		MODIFIED 12/21/2005	
BY NOS		ATTORNEYS	
SPF		JCM	
SPF		SPF	

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10/625,573	Semiconductor memory device
Transaction History	
Date	Content
11-09-2005	Mail Notice of Restarted Respo
03-23-2005	Mail Final Rejection (PTOL - 32
02-03-2005	Date Forwarded to Examiner
01-21-2005	Workflow Incoming amendmen
10-21-2004	Non-Final Rejection
08-13-2004	Response to Election / Restrict
08-24-2004	Mail Restriction Requirement
07-24-2003	Reference capture on IDS
07-24-2003	Request for Foreign Priority (P
07-31-2004	Case Docketed to Examiner in
06-22-2004	Application Return TO OIPE
06-21-2004	Application is Now Complete

HITA-0415USA		UNITED STATES		TITLE		SEMICONDUCTOR MEMORY DEVICE		PRIORITY		07/26/2002		07/24/2003		07/24/2003	
COUNTRY	US	NEW	10/625,573	PATENT#		STATUS	PENDING	FILE		ISSUE		1ST		EXP	
RELATED		UTL													
TYPE	HITA	HITACHI													
CLIENT															
AGENT															

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PU	N	PUBLICATION		18/M	01/26/2004			0	01/26/2004		1/M	Y	Y
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RT	N	RESPONSE TO OA (1MO)		1/M	08/24/2004	5	1	0	09/15/2004		0/M	Y	Y
AP	N	AMEND PREPUBLICATION		17/M	12/24/2004			0	12/24/2004		1/M	Y	Y
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SC	N	STATUS CHECK		18/M	01/24/2005			0	01/24/2005		1/M	Y	Y
ER	N	EARLY RESP FINAL REJ		2/M	05/23/2005			0	05/23/2005		1/M	Y	Y
SC	N	STATUS CHECK		1/M	10/27/2005			0	10/27/2005		1/M	Y	Y
Check whether final rejection re-mailed (was never received); examiner to mail re-started OA per 11/3/05 telephone call													
NA	N	NOTICE OF APPEAL		3/M	02/09/2006	3	1	0	03/09/2006		1/M	Y	Y
FR	N	FINAL REJECTION		3/M	02/09/2006	3	1	0	05/09/2006		1/M	Y	Y

INVENTORS		ASSIGNEES	
SAKAMOTO, Masatoshi		HITACHI, LTD.	
HASEGAWA, Masatoshi			

USER-DEFINABLE FIELDS		PATENT FIELDS	
PUB#		SMALL ENTITY	NO
TX12		CLAIMS	
CASE		PUBLICATION#	
ENTERED 07/09/2003		MODIFIED 01/30/2006	
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		JCM	
		SPT	

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ReedSmith^{LLP}

TO Examiner Khlem Nguyen

Company U.S. Patent and Trademark Office

Fax (571) 273-1865

Phone (571) 272-1865

FROM

Nancy DeSanti for Stanley P. Fisher

Phone 703.641.4547

Fax 703-204-1352

Date February 2, 2006

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NOTES: Examiner Nguyen: Per our telephone call on 1/31/06 regarding U.S. patent application 10/020,314, enclosed please find the papers for submission to your SPE in our request to re-start the date for your office action (a copy of which you faxed us on 1/4/05 with a formal copy to follow which we still have not received). Also enclosed please find the information and docketing sheets regarding two other items that were supposed to have been mailed to us from the USPTO on 3/23/05 (the same date as your office action according to the PAIR System) but which we also never received. Would you please contact us after you have reviewed the enclosed papers. Thank you very much.

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Operator					

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